

Please amend the present application as follows:

Specification

Please substitute the following clean copy paragraph/page text for the pending paragraph/page text of the same number. The following is a marked-up version of the specification with the language that is underlined (“ ”) being added and the language that contains strikethrough (“”) being deleted:

Please amend the paragraph starting on p. 20, line 20 as follows:

In the operation of amplitude control circuit 802, peak detector 1100 receives the output signal of controllable oscillator 1002 on connection 804. Peak detector 1100 determines the amplitude of the output signal and provides the information on connection 1108 to low pass filter 1102 where the signal is filtered. The filtered signal is provided on connection 1110 to amplifier 1104. Amplifier 1104 compares the information associated with the amplitude of the output signal of controllable oscillator 202 to a reference voltage 1112 and provides a control bit on connection 1114 to binary state search module 1106. For example, amplifier 1104 may be configured to generate a logic 1 for the control bit when the amplitude of the output signal of controllable oscillator 202 is higher than the reference voltage and a logic 0 for the control bit when the reference voltage is higher than the amplitude of the output signal. Where amplitude control circuit 802 operates in conjunction with frequency control circuit 208, amplifier 1104 may be connected to, and controlled by, frequency control ~~signal~~ circuit 208 via connection 805. Referring again to FIG. 7 11, binary state search module 1106 functions the same way as described above with respect to binary state search module 508. Again, where amplitude control circuit 802 operates in conjunction with frequency control circuit 208, binary state search module 1106 may be connected to, and controlled by, frequency control signal 208 via connection 805.